

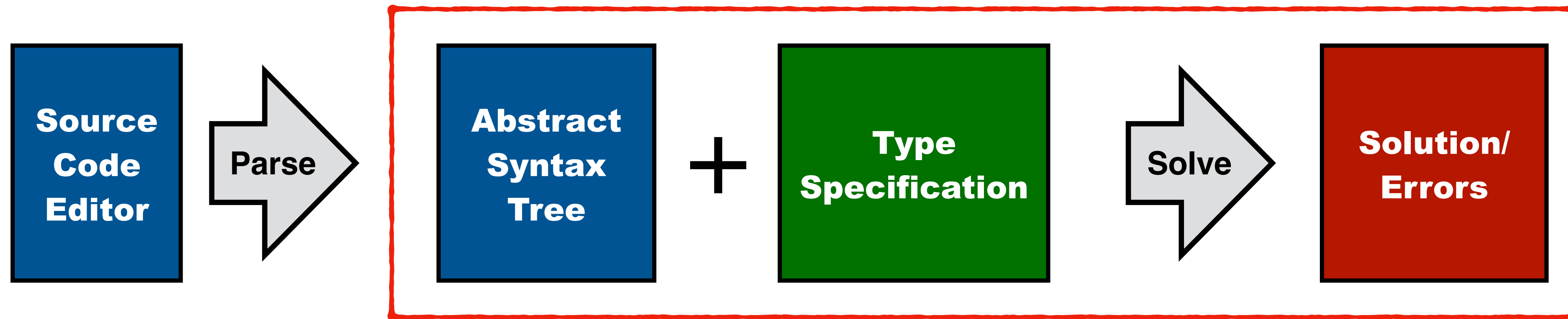
Constraint Semantics and Constraint Resolution

Hendrik van Antwerpen
Eelco Visser



CS4200 | Compiler Construction | September 30, 2021

This lecture



- Type checking with type specifications
- Semantics of a type specification
- Type checking algorithms
- Constraint solving for type specifications
- Term equality and unification

Reading Material

The following papers add background, conceptual exposition, and examples to the material from the slides. Some notation and technical details have been changed; check the documentation.

This paper introduces the Statix DSL for definition of type systems.

Shows how to use scope graphs for structural type and generic types

Explains the need for scheduling in type checkers

OOPSLA 2018

<https://doi.org/10.1145/3276484>

Scopes as Types

HENDRIK VAN ANTWERPEN, Delft University of Technology, Netherlands
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Scope graphs are a promising generic framework to model the binding structures of programming languages, bridging formalization and implementation, supporting the definition of type checkers and the automation of type safety proofs. However, previous work on scope graphs has been limited to simple, nominal type systems. In this paper, we show that viewing *scopes as types* enables us to model the internal structure of types in a range of non-simple type systems (including structural records and generic classes) using the generic representation of scopes. Further, we show that relations between such types can be expressed in terms of generalized scope graph queries. We extend scope graphs with scoped relations and queries. We introduce Statix, a new domain-specific meta-language for the specification of static semantics, based on scope graphs and constraints. We evaluate the scopes as types approach and the Statix design in case studies of the simply-typed lambda calculus with records, System F, and Featherweight Generic Java.

CCS Concepts: • **Software and its engineering** → **Semantics; Domain specific languages**;

Additional Key Words and Phrases: static semantics, type system, type checker, name resolution, scope graphs, domain-specific language

ACM Reference Format:

Hendrik van Antwerpen, Casper Bach Poulsen, Arjen Rouvoet, and Eelco Visser. 2018. Scopes as Types. *Proc. ACM Program. Lang.* 2, OOPSLA, Article 114 (November 2018), 30 pages. <https://doi.org/10.1145/3276484>

1 INTRODUCTION

The goal of our work is to support high-level specification of type systems that can be used for multiple purposes, including reasoning (about type safety among other things) and the implementation of type checkers [Visser et al. 2014]. Traditional approaches to type system specification do not reflect the commonality underlying the name binding mechanisms for different languages. Furthermore, operationalizing name binding in a type checker requires carefully staging the traversals of the abstract syntax tree in order to collect information before it is needed. In this paper, we introduce an approach to the declarative specification of type systems that is close in abstraction to traditional type system specifications, but can be directly interpreted as type checking rules. The approach is based on scope graphs for name resolution, and constraints to separate traversal order from solving order.

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2475-1421/2018/11-ART114
<https://doi.org/10.1145/3276484>

Formalizes the declarative and operational semantics of Statix Core

Introduces concept of critical edges to determine whether a query can be executed

Extends the type system of Statix with ownership in order to statically guarantee that critical edges can be computed

OOPSLA 2020

<https://doi.org/10.1145/3428248>



Knowing When to Ask

Sound Scheduling of Name Resolution in Type Checkers Derived from Declarative Specifications

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There is a large gap between the specification of type systems and the implementation of their type checkers, which impedes reasoning about the soundness of the type checker with respect to the specification. A vision to close this gap is to automatically obtain type checkers from declarative programming language specifications. This moves the burden of proving correctness from a case-by-case basis for concrete languages to a single correctness proof for the specification language. This vision is obstructed by an aspect common to all programming languages: name resolution. Naming and scoping are pervasive and complex aspects of the static semantics of programming languages. Implementations of type checkers for languages with name binding features such as modules, imports, classes, and inheritance interleave collection of binding information (i.e., declarations, scoping structure, and imports) and querying that information. This requires scheduling those two aspects in such a way that query answers are stable—i.e., they are computed only after all relevant binding structure has been collected. Type checkers for concrete languages accomplish stability using language-specific knowledge about the type system.

In this paper we give a language-independent characterization of necessary and sufficient conditions to guarantee stability of name and type queries during type checking in terms of *critical edges in an incomplete scope graph*. We use critical edges to give a formal small-step operational semantics to a declarative specification language for type systems, that achieves soundness by delaying queries that may depend on missing information. This yields type checkers for the specified languages that are sound by construction—i.e., they schedule queries so that the answers are stable, and only accept programs that are name- and type-correct according to the declarative language specification. We implement this approach, and evaluate it against specifications of a small module and record language, as well as subsets of Java and Scala.

CCS Concepts: • **Theory of computation** → **Constraint and logic programming; Operational semantics**.

Additional Key Words and Phrases: Name Binding, Type Checker, Statix, Static Semantics, Type Systems

ACM Reference Format:

Arjen Rouvoet, Hendrik van Antwerpen, Casper Bach Poulsen, Robbert Krebbers, and Eelco Visser. 2020. Knowing When to Ask: Sound Scheduling of Name Resolution in Type Checkers Derived from Declarative Specifications. *Proc. ACM Program. Lang.* 4, OOPSLA, Article 180 (November 2020), 28 pages. <https://doi.org/10.1145/3428248>

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2475-1421/2020/11-ART180
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Proc. ACM Program. Lang., Vol. 4, No. OOPSLA, Article 180. Publication date: November 2020.

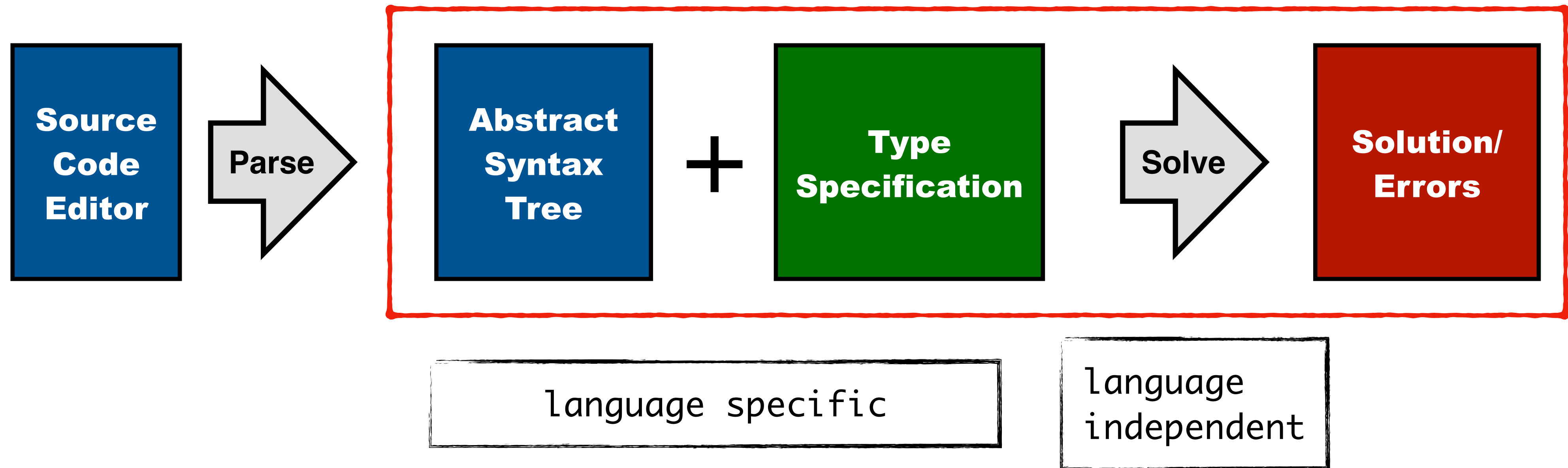
Good introduction to unification, which is the basis of many type inference approaches, constraint languages, and logic programming languages. Read sections 1, and 2.

Baader et al. “Chapter 8 - Unification Theory.” In Handbook of Automated Reasoning, 445–533. Amsterdam: North-Holland, 2001.

<https://www.cs.bu.edu/~snyder/publications/UnifChapter.pdf>

CHAPTER 8	
Unification theory	
Franz Baader	
Wayne Snyder	
SECOND READERS: Paliath Narendran, Manfred Schmidt-Schauss, and Klaus Schulz.	
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HANDBOOK OF AUTOMATED REASONING	
Edited by Alan Robinson and Andrei Voronkov	
© Elsevier Science Publishers B.V., 2001	

Type Checking with Specifications



Typing Rules

What are typing rules?

- Predicates that specify constraints (rule premises) on their arguments (the program)
- Syntax-directed, match on program constructs (at least in Statix)
- Specification of what it means to be well-typed!

What are the premises?

- Logical assertions that should hold for well-typed programs
- Specification language determines what assertions can be made
 - Type equality and inequality, name resolution, ...
- Determines the expressiveness of the specification!

Solving

- Given an initial predicate that must hold, ...
- find an assignment for all logical variables, such that the predicate is satisfied

Typing Checking

Challenges for type checker implementations?

- Collecting (non-lexical) binding information before use
- Dealing with unknown (type) values

Separation of what from how

- Typing rules says what is a well-typed program
- Solver says how to determine that a program is well-typed

Separation of computation from program structure

- Typing rules follow the structure of the program
- Solver is flexible in order of resolution

Approach: reusable solver for the specification language

- Support logical variables for unknowns and infer their values
- Automatically determine correct resolution order

Constraint Semantics

What gives constraints meaning?

What is the meaning of constraints?

- What is a valid solution?
- Or: in which models are the constraints satisfied?
- Can we describe this independent of an algorithm to find a solution?

```
ty == FUN(ty1,ty2)  
Var{x} in s l-> d  
ty1 == INT()
```

When are constraints satisfied?

- Formally described by the declarative semantics
- Written as $G, \phi \models C$
- Satisfied in a model
 - Substitution ϕ (read: phi)
 - Scope graph G
- Describes for every type of constraint when it is satisfied

Semantics of (a Subset of) Statix Constraints

Syntax

```
C = t == t           // equality
    | r in s l-> d     // name resolution (short for query var ... in s l-> [d])
    | C /\ C          // conjunction
```

Declarative semantics

$G, \phi \models t == u$	if $\phi(t) = \phi(u)$
$G, \phi \models r \text{ in } s \text{ l-> } d$	if $\phi(r) = x$ and $\phi(d) = x$ and $\phi(s) = \#i$ and x resolves to x from $\#i$ in G
$G, \phi \models C_1 /\wedge C_2$	if $G, \phi \models C_1$ and $G, \phi \models C_2$

Using the Semantics

Program

```
let
  function f1(i2 : int) : int =
    i3 + 1
in
  f4(14)
end
```

Program constraints

```
ty1 == INT()
INT() == INT()
"i" in #s1 |-> d1
ty2 == INT()
"f" in #s0 |-> d2
ty3 == FUN(ty4, ty5)
ty4 == INT()
...
```

Unifier ϕ (model)

```
 $\phi = \{ \text{ty}_1 \rightarrow \text{INT}(),$   
           $\text{ty}_2 \rightarrow \text{INT}(),$   
           $\text{ty}_3 \rightarrow \text{FUN}(\text{INT}(), \text{ty}_5),$   
           $\text{ty}_4 \rightarrow \text{INT}(),$   
           $d_1 \rightarrow \text{"i"},$   
           $d_2 \rightarrow \text{"f"}$   
           $\}$ 
```

Constraint semantics

$G, \phi \models t == u$

if $\phi(t) = \phi(u)$

$G, \phi \models r \text{ in } s \mid\rightarrow d$

if $\phi(r) = x$

and $\phi(d) = x$

and $\phi(s) = \#i$

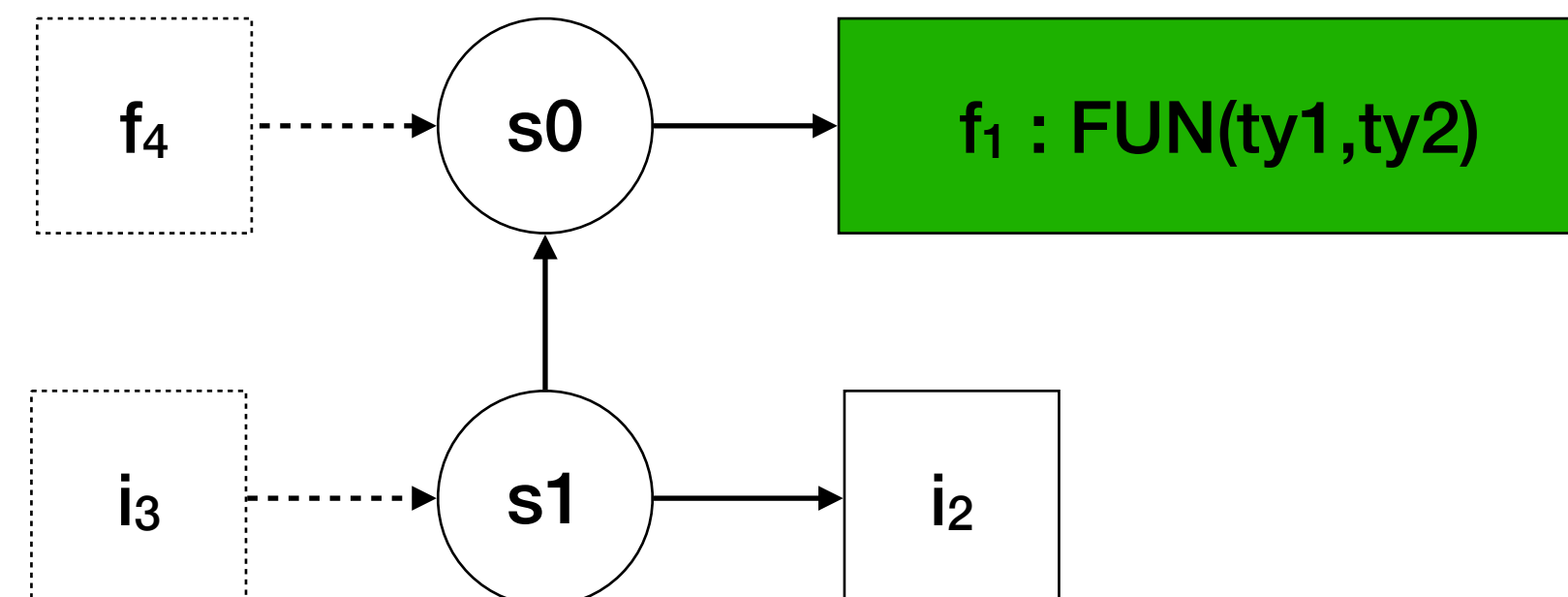
and x resolves to x from $\#i$ in G

$G, \phi \models C_1 /\backslash C_2$

if $G, \phi \models C_1$

and $G, \phi \models C_2$

Scope graph G (model)



Different Kinds of Variables

Program

```
let
  function f1(i2 : int) : int =
    i3 + 1
in
  f4(14)
end
```

Program constraints

```
ty1 == INT()
INT() == INT()
"i" in #s1 |-> d1
ty2 == INT()
"f" in #s0 |-> d2
ty3 == FUN(ty4,ty5)
ty4 == INT()
...
```

Unifier ϕ (model)

```
 $\phi = \{ \text{ty1} \rightarrow \text{INT}(),$   
           $\text{ty2} \rightarrow \text{INT}(),$   
           $\text{ty3} \rightarrow \text{FUN}(\text{INT}(), \text{ty5}),$   
           $\text{ty4} \rightarrow \text{INT}(),$   
           $d1 \rightarrow \text{"i"},$   
           $d2 \rightarrow \text{"f"}$   
           $\}$ 
```

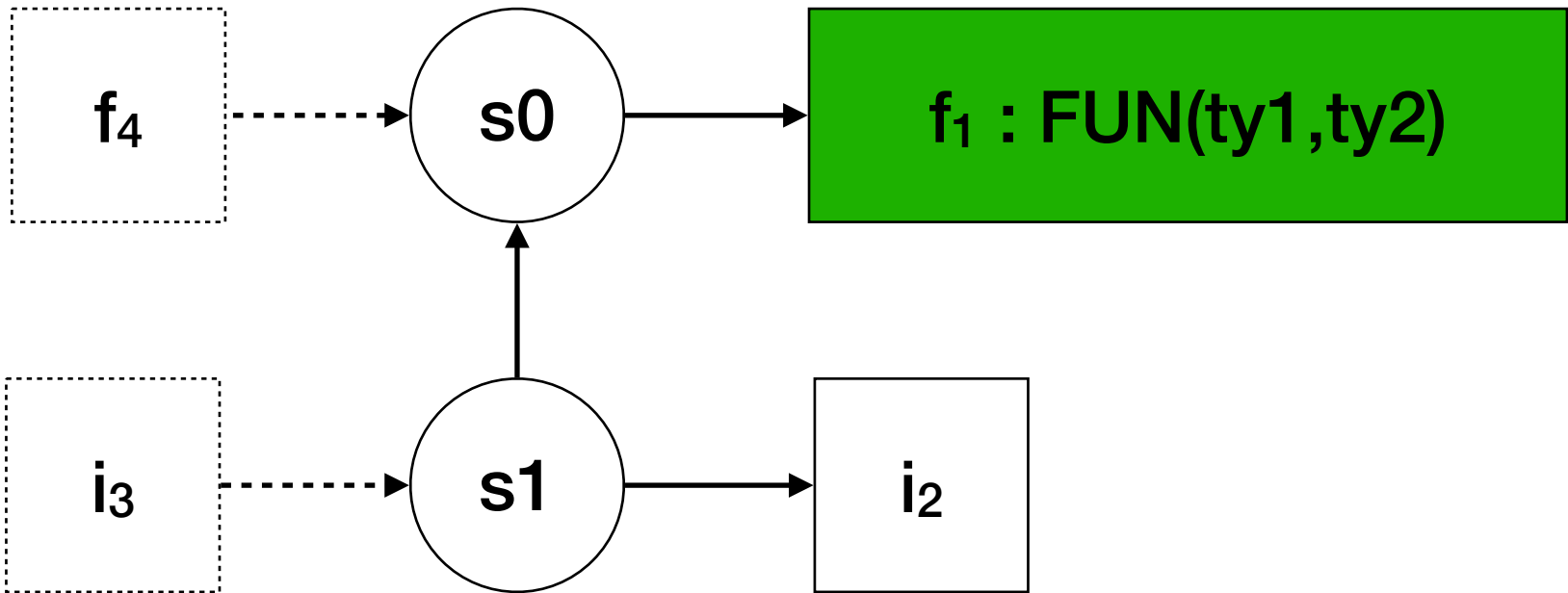
Constraint semantics

$G, \phi \models t == u$
 if $\phi(t) = \phi(u)$

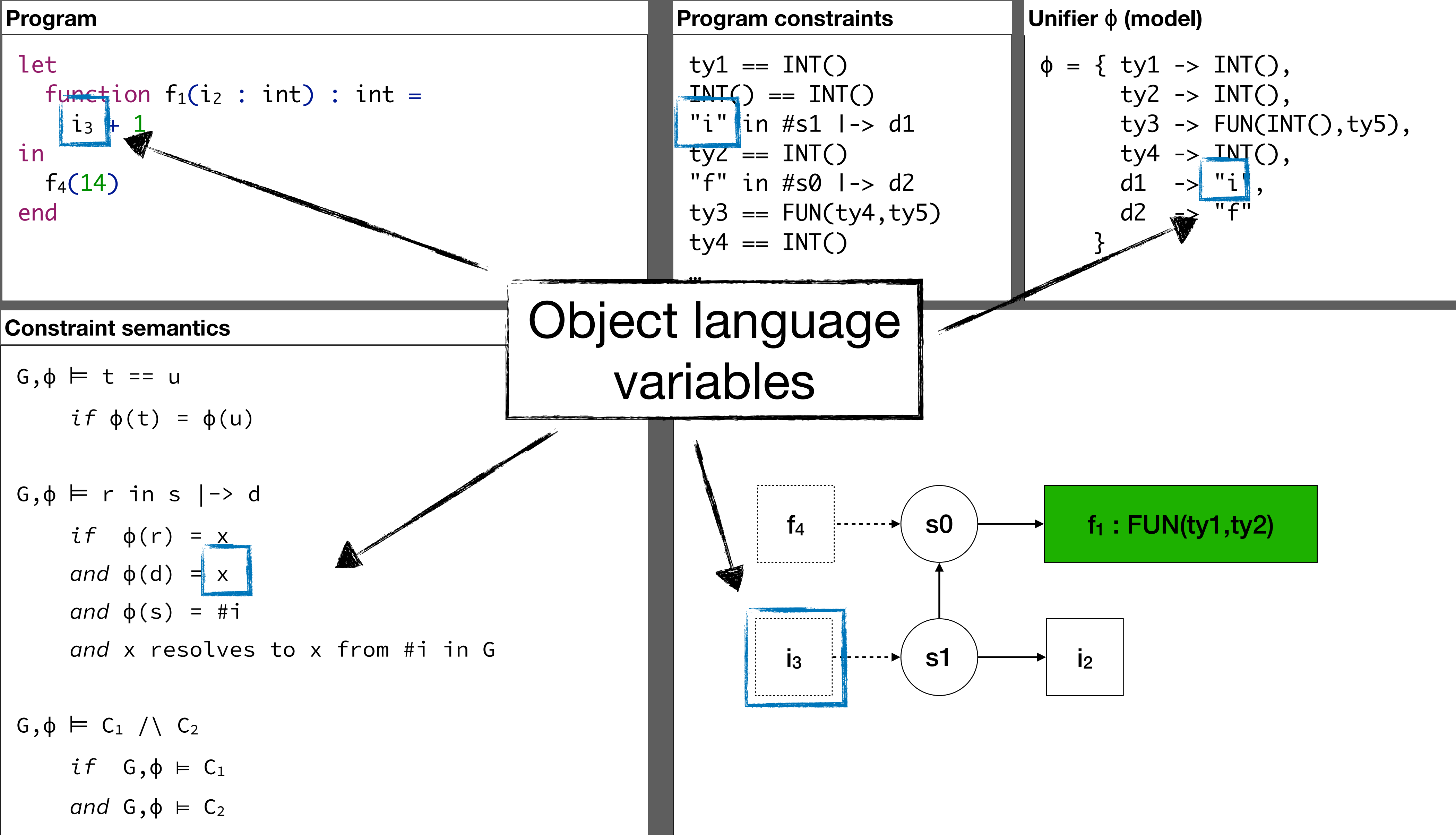
$G, \phi \models r \text{ in } s \mid\rightarrow d$
 if $\phi(r) = x$
 and $\phi(d) = x$
 and $\phi(s) = \#i$
 and x resolves to x from $\#i$ in G

$G, \phi \models C_1 /\backslash C_2$
 if $G, \phi \models C_1$
 and $G, \phi \models C_2$

Scope graph G (model)



Different Kinds of Variables



Different Kinds of Variables

Program	Program constraints	Unifier ϕ (model)
<pre>let function f₁(i₂ : int) : int = i₃ + 1 in f₄(14) end</pre>	<pre>ty1 == INT() INT() == INT() "i" in #s1 l-> d1 ty2 == INT() "f" in #s0 l-> d2 ty3 == FUN(ty4, ty5) ty4 == INT() ...</pre>	<pre>$\phi = \{$ ty1 -> INT(), ty2 -> INT(), ty3 -> FUN(INT(), ty5), ty4 -> INT(), d1 -> "i", d2 -> "f" $\}$</pre>
Constraint semantics	Constraint / logic variables	
<p>$G, \phi \models t == u$ if $\phi(t) = \phi(u)$</p> <p>$G, \phi \models r \text{ in } s \mid\rightarrow d$ if $\phi(r) = x$ and $\phi(d) = x$ and $\phi(s) = \#i$ and x resolves to x from $\#i$ in G</p> <p>$G, \phi \models C_1 /\backslash C_2$ if $G, \phi \models C_1$ and $G, \phi \models C_2$</p>	<pre>graph TD f4[f4] -.-> s0((s0)) i3[i3] -.-> s1((s1)) s0 --> f1[f1 : FUN(ty1, ty2)] s1 --> i2[i2]</pre>	

Different Kinds of Variables

Program

```
let
  function f1(i2 : int) : int =
    i3 + 1
in
  f4(14)
end
```

Program constraints

```
ty1 == INT()
INT() == INT()
"i" in #s1 |-> d1
ty2 == INT()
"f" in #s0 |-> d2
ty3 == FUN(ty4, ty5)
ty4 == INT()
...
```

Unifier ϕ (model)

```
 $\phi = \{ \text{ty}_1 \rightarrow \text{INT}(),$   
           $\text{ty}_2 \rightarrow \text{INT}(),$   
           $\text{ty}_3 \rightarrow \text{FUN}(\text{INT}(), \text{ty}_5),$   
           $\text{ty}_4 \rightarrow \text{INT}(),$   
           $d_1 \rightarrow \text{"i"},$   
           $d_2 \rightarrow \text{"f"}$   
           $\}$ 
```

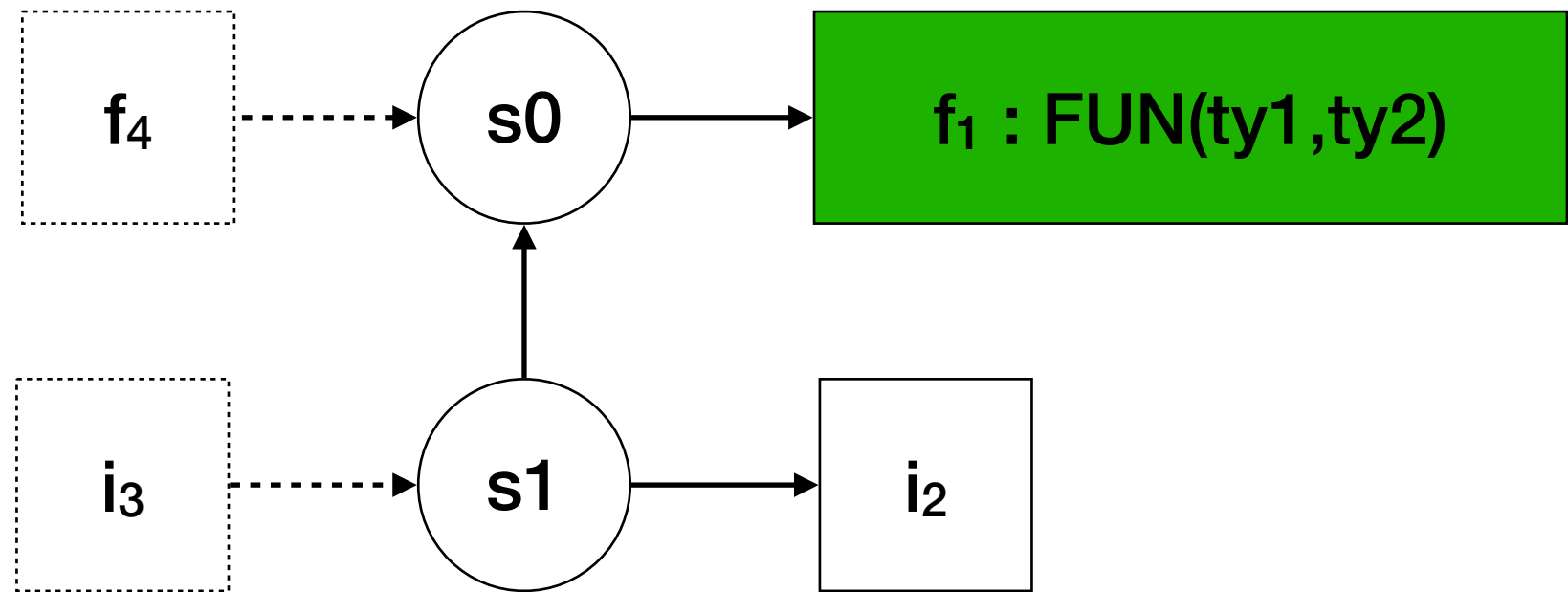
Constraint semantics

$G, \phi \models t == u$
if $\phi(t) = \phi(u)$

$G, \phi \models r \text{ in } s \text{ } |-> d$
if $\phi(r) = x$
and $\phi(d) = x$
and $\phi(s) = \#i$
and x resolves to x from $\#i$ in G

$G, \phi \models C_1 /\backslash C_2$
if $G, \phi \models C_1$
and $G, \phi \models C_2$

Semantics meta-variables



Type Checking

How to check types?

What should a type checker do?

- Check that a program is well-typed!
- Resolve names, and check or compute types
- Report useful error messages
- Provide a representation of name and type information
 - Type annotated AST

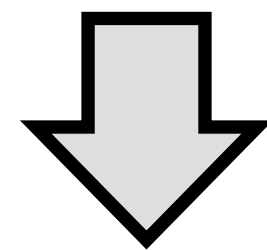
This information is used for

- Next compiler steps (optimization, code generation, ...)
- IDE (error reporting, code completion, refactoring, ...)
- Other tools (API documentation, ...)

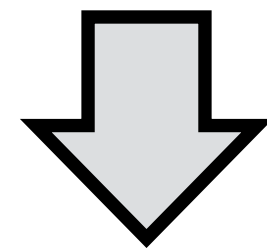
How are type checkers implemented?

Computing Type of Expression (recap)

```
function (a : int) = a + 1
```



```
Fun("a", INT(),  
    Plus(Var("a"), Int(1)))
```



```
FUN(INT(), INT())
```

```
typeOfExp(s, Int(_)) = INT().
```

```
typeOfExp(s, Plus(e1, e2)) = INT() :-  
    typeOfExp(s, e1) == INT(),  
    typeOfExp(s, e2) == INT().
```

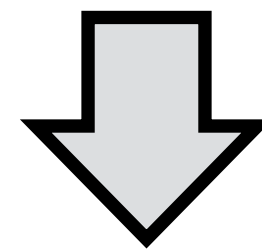
```
typeOfExp(s, Fun(x, te, e)) = FUN(S, T) :- {s_fun}  
    typeOfTypeExp(s, te) == S,  
    new s_fun, s_fun -P-> s,  
    s_fun -> Var{x} with typeOfDecl S,  
    typeOfExp(s_fun, e) == T.
```

```
typeOfExp(s, Var(x)) = T :-  
    typeOfDecl of Var{x} in s l-> [(_, (_, T))].
```

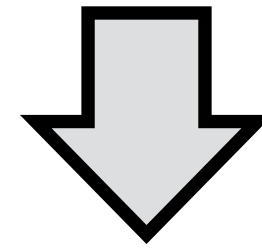
- Can be executed top down, in premise order
- Could be written almost like this in a functional language

Inferring the Type of a Parameter

```
function (a         ) = a + 1
```



```
Fun("a",             
    Plus(Var("a"), Int(1)))
```



FUN(INT(), INT())

Unknown S!

```
typeOfExp(s, Int(_)) = INT().
```

```
typeOfExp(s, Plus(e1, e2)) = INT() :-  
    typeOfExp(s, e1) == INT(),  
    typeOfExp(s, e2) == INT().
```

```
typeOfExp(s, Fun(x, e)) = FUN(S, T) :- {s_fun}
new s_fun, s_fun -P-> s,
s_fun -> Var{x} with typeOfDecl S,
typeOfExp(s_fun, e) == T.
```

```
typeOfExp(s, Var(x)) = T :-  
  typeOfDecl of Var{x} in s |-> [(_, (x, T))].
```

- What are the consequences for our typing rules?
- Types are not known from the start, but learned gradually
- A simple top-down traversal is insufficient

Checking classes

```
class A {  
    B m() {  
        return new C();  
    }  
}  
  
class B {  
    int i;  
}  
  
class C extends B {  
    int m(A a) {  
        return a.m().i;  
    }  
}
```

How can we type check this program?

- Is there a possible single traversal strategy here?
- Why are the type annotations not enough?
- What strategy could be used?

Two-pass approach

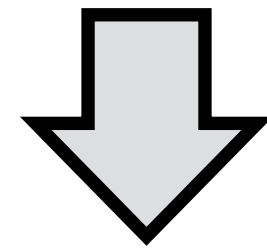
- The first pass builds a class table
- The second pass checks expressions using the class table

Question

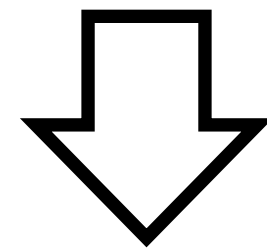
- Does this still work if we introduce nested classes?

Variables and Constraints

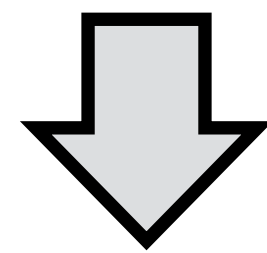
```
function (a         ) = a + 1
```



```
Fun("a",             
    Plus(Var("a"), Int(1)))
```



`FUN(?S, INT()) + ?S == INT()`



?S := INT○

```
typeOfExp(s, Int(_)) = INT().
```

```
typeOfExp(s, Plus(e1, e2)) = INT() :-  
    typeOfExp(s, e1) == INT(),  
    typeOfExp(s, e2) == INT().
```

```
typeOfExp(s, Fun(x,            e)) = FUN(S, T) :- {s_fun}
                                  
new s_fun, s_fun -P-> s,  
s_fun -> Var{x} with typeOfDecl S,  
typeOfExp(s_fun, e) == T.
```

```
typeOfExp(s, Var(x)) = T :-  
  typeOfDecl of Var{x} in s |-> [(_, (T))].
```

How to check types?

What are challenges when implementing a type checker?

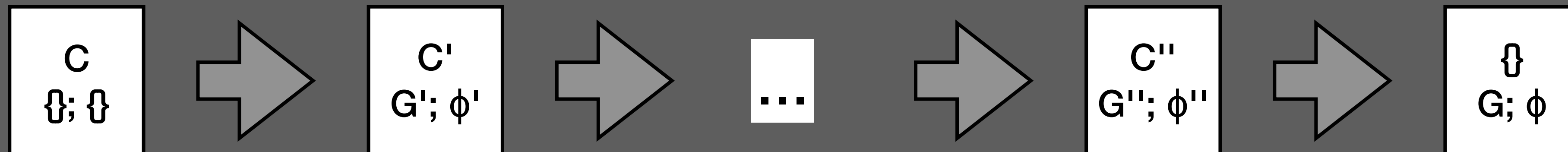
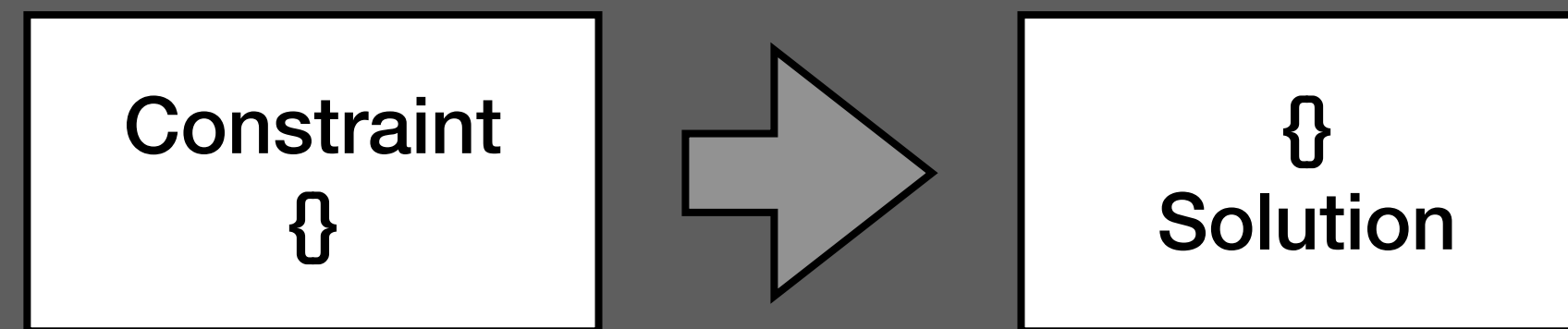
- Collecting necessary binding information before using it
- Gradually learning type information

What are the consequences of these challenges?

- The order of computation needs to be more flexible than the AST traversal
- Support explicit logical variables during solving

Solving Constraints

Solving by Rewriting



Solving by Rewriting


Non-deterministic
constraint selection



$$\langle C; G, \phi \rangle \longrightarrow \langle C; G, \phi \rangle$$

$$\langle t = u, C; G, \Phi \rangle \longrightarrow \langle C; G, \Phi' \rangle \text{ where } \text{unify}(\Phi, t, u) = \Phi'$$

$$\langle s1 \text{ -L} \rightarrow s2, C; G, \Phi \rangle \longrightarrow \langle C; G', \Phi \rangle \text{ where } \Phi(s1) = \#i, \Phi(s2) = \#j, \\ G + \{\#i \text{ -L} \rightarrow \#j\} = G'$$

$$\langle r \text{ in } s \mapsto t, C; G, \Phi \rangle \longrightarrow \langle t = d, C; G, \Phi \rangle \text{ where } \Phi(r) = x, \Phi(s) = \#i, \\ \text{resolve}(G, \#i, x) = d$$


```
def solve(C):  
    if  $\langle C; \{\}, \{\} \rangle \longrightarrow^* \langle \{\}; G, \Phi \rangle$ :  
        return  $\langle G, \Phi \rangle$   
    else:  
        fail
```

Scope graph and
name resolution
algorithm don't have
to know about logical
variables

Solving by Rewriting

Solver = rewrite system

- Rewrite a constraint set + solution
- Simplifying and eliminating constraints
 - Constraint selecting is non-deterministic
 - Resolution order is controlled by side conditions on rewrite rules
- Rely on (other) solvers and algorithms for base cases
 - Unification for term equality
 - Scope graph resolution
- The solution is final if all constraints are eliminated

Does the order matter for the outcome?

- Confluence: the output is the same for any solving order
- Partly true for Statix
 - Up to variable and scope names
 - Only if all constraints are reduced

What is the difference?

- Algorithm computes a solution (= model)
- Semantics describes when a constraint is satisfied by a model

How are these related?

- Soundness
 - ▶ If the solver returns $\langle G, \phi \rangle$, then $G, \phi \models C$
- Completeness:
 - ▶ If a G and ϕ exists such that $G, \phi \models C$, then the solver returns it
 - ▶ If no such G or ϕ exists, the solver fails
- Principality
 - ▶ The solver finds the most general ϕ

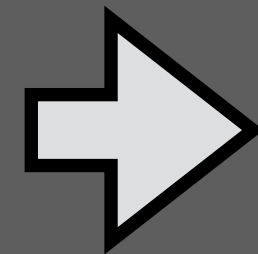
Term Equality & Unification

Syntactic Terms

Generic Terms

terms	t, u
functions	f, g, h

INT()
FUN(INT(), INT())



function symbol

arguments

$f(t_0, \dots, t_n)$

arity

Syntactic Equality

$f(t_0, \dots, t_n) == g(u_0, \dots, u_m)$ if

- $f = g$, and $n = m$
- $t_i == u_i$ for every i

Variables and Substitution

terms	t, u
functions	f, g, h
variables	a, b, c
substitution	ϕ

variable

$f(g(), \underline{a})$

substitution

$\phi = \{ \underline{a} \rightarrow f(g(), b), \underline{b} \rightarrow h() \}$

domain

$\phi(a) = t$ if $\{ a \rightarrow t \}$ in ϕ
 $\phi(a) = a$ otherwise
 $\phi(f(t_0, \dots, t_n)) = f(\phi(t_0), \dots, \phi(t_n))$

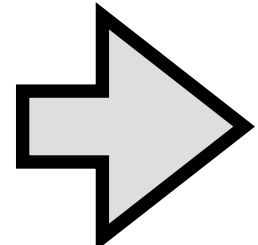
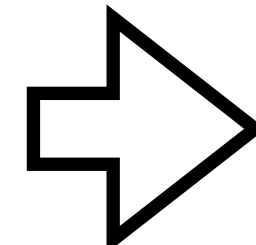
$f(g(), f(g(), b))$

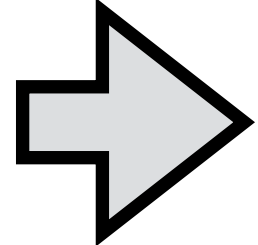
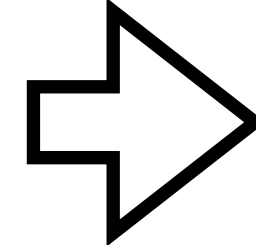
ground term: a term without variables

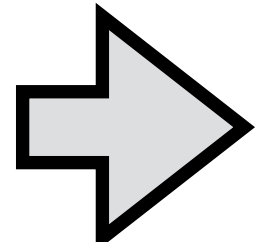
Unifiers

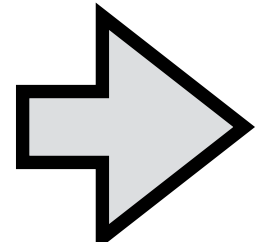

terms	t, u
functions	f, g, h
variables	a, b, c
substitution	ϕ

unifier: a substitution that makes terms equal

$f(a, g()) == f(h(), b)$  $\begin{matrix} a \rightarrow h() \\ b \rightarrow g() \end{matrix}$  $f(h(), g()) == f(h(), g())$

$g(a, f(b)) == g(f(h()), a)$  $\begin{matrix} a \rightarrow f(h()) \\ b \rightarrow h() \end{matrix}$  $g(f(h()), f(h())) == g(f(h()), f(h()))$

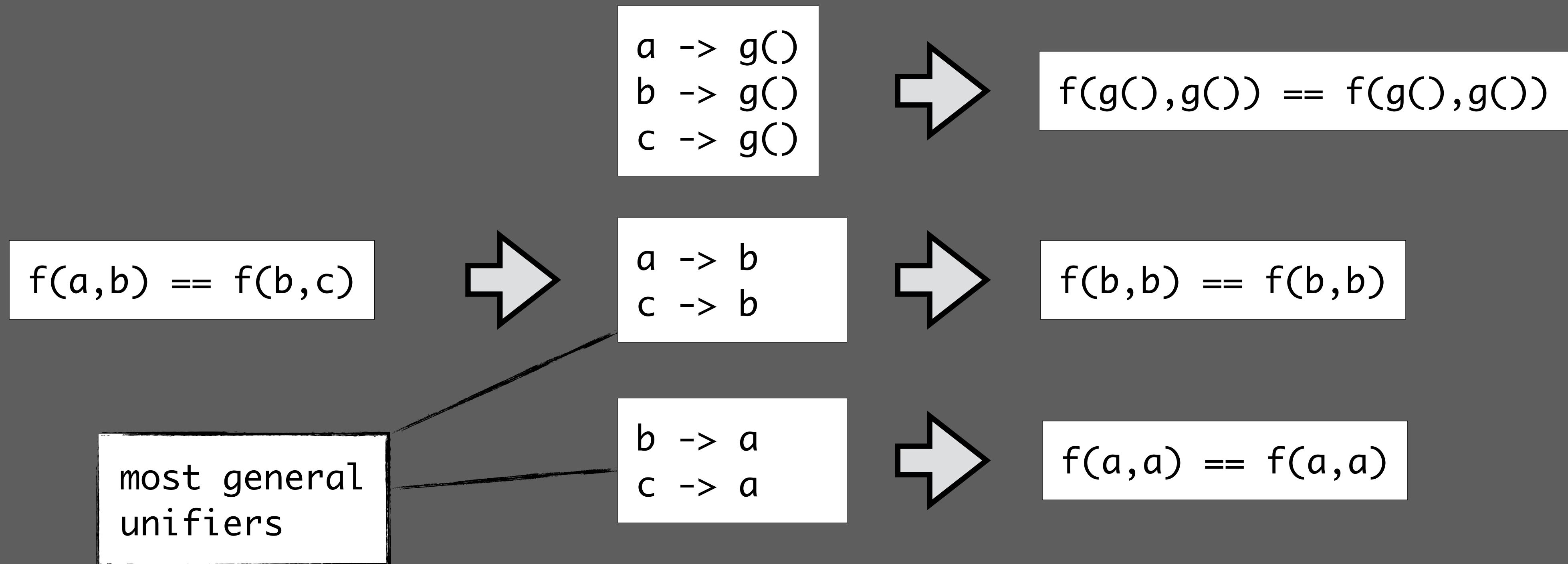
$f(a, h()) == g(h(), b)$  no unifier, $f \neq g$

$f(b, b) == b$  $b \rightarrow f(b, b)$ 

not idempotent

Most General Unifiers

terms	t, u
functions	f, g, h
variables	a, b, c
substitution	ϕ



Most General Unifiers

terms	t, u
functions	f, g, h
variables	a, b, c
substitution	ϕ

every unifier is an instance of a most general unifier

(implicit) identity case

$a \rightarrow b$
 $b \rightarrow b$
 $c \rightarrow b$

$b \rightarrow g()$

$a \rightarrow g()$
 $b \rightarrow g()$
 $c \rightarrow g()$

most general unifiers are related by renaming substitutions

$a \rightarrow b$
 $b \rightarrow b$
 $c \rightarrow b$

$b \rightarrow a$

$a \rightarrow a$
 $b \rightarrow a$
 $c \rightarrow a$

$a \rightarrow a$
 $b \rightarrow a$
 $c \rightarrow a$

$a \rightarrow b$

$a \rightarrow b$
 $b \rightarrow b$
 $c \rightarrow b$

Unification

```
global  $\phi$ 
def unify(t, u):
    if t is a variable:
         $t := \phi(t)$ 
    if u is a variable:
         $u := \phi(u)$ 
    if t is a variable and  $t == u$ :
        pass
    else if  $t == f(t_1, \dots, t_n)$  and  $u == g(u_1, \dots, u_m)$ :
        if  $f == g$  and  $n == m$ :
            for  $i := 1$  to  $n$ :
                unify( $t_i$ ,  $u_i$ )
        else:
            fail "different function symbols"
    else if t is not a variable:
        unify(u, t)
    else if t occurs in u:
        fail "recursive term"
    else:
         $\phi += \{ t \rightarrow u \}$ 
```

terms	t, u
functions	f, g, h
variables	a, b, c
substitution	ϕ

$t == a$
instantiate variable

$u == b$
instantiate variable

$b == b$
equal variables

$t == f(t_1, \dots, t_5), u == f(u_1, \dots, u_5)$
matching terms

$t == f(t_0, \dots, t_5), u == g(u_0, \dots, u_3)$
mismatching terms

$t == f(t_0, \dots, t_5), u == b$
swap terms

$t == a, u == k(g(a, f()))$
recursive terms

$t == a, u == k(u_0, \dots, u_5)$
extend unifier

Properties of Unification

Soundness

- If the algorithm returns a unifier, it makes the terms equal

Completeness

- If a unifier exists, the algorithm will return it

Principality

- If the algorithm returns a unifier, it is a most general unifier

Termination

- The algorithm always returns a unifier or fails

Efficient Unification with Union-Find

Complexity of Unification

Space complexity

- Exponential
- Representation of unifier

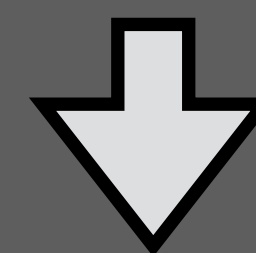
Time complexity

- Exponential
- Recursive calls on terms

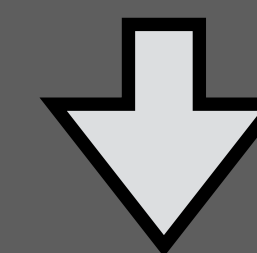
Solution

- Union-Find algorithm
- Complexity growth can be considered constant

terms	t, u
functions	f, g, h
variables	a, b, c
substitution	ϕ

$$h(a_1, \dots, a_n, f(b_0, b_0), \dots, f(b_{n-1}, b_{n-1}), a_n) ==$$
$$h(f(a_0, a_0), \dots, f(a_{n-1}, a_{n-1}), b_1, \dots, b_{n-1}, b_n)$$

$$\begin{aligned} a_1 &\rightarrow f(a_0, a_0) \\ a_2 &\rightarrow f(f(a_0, a_0), f(a_0, a_0)) \\ a_i &\rightarrow \dots 2^{i+1}-1 \text{ subterms } \dots \\ b_1 &\rightarrow f(a_0, a_0) \\ b_2 &\rightarrow f(f(a_0, a_0), f(a_0, a_0)) \\ b_i &\rightarrow \dots 2^{i+1}-1 \text{ subterms } \dots \end{aligned}$$

fully applied


$$\begin{aligned} a_1 &\rightarrow f(a_0, a_0) \\ a_2 &\rightarrow f(a_1, a_1) \\ a_i &\rightarrow \dots 3 \text{ subterms } \dots \\ b_1 &\rightarrow f(a_0, a_0) \\ b_2 &\rightarrow f(a_1, a_1) \\ b_i &\rightarrow \dots 3 \text{ subterms } \dots \end{aligned}$$

triangular

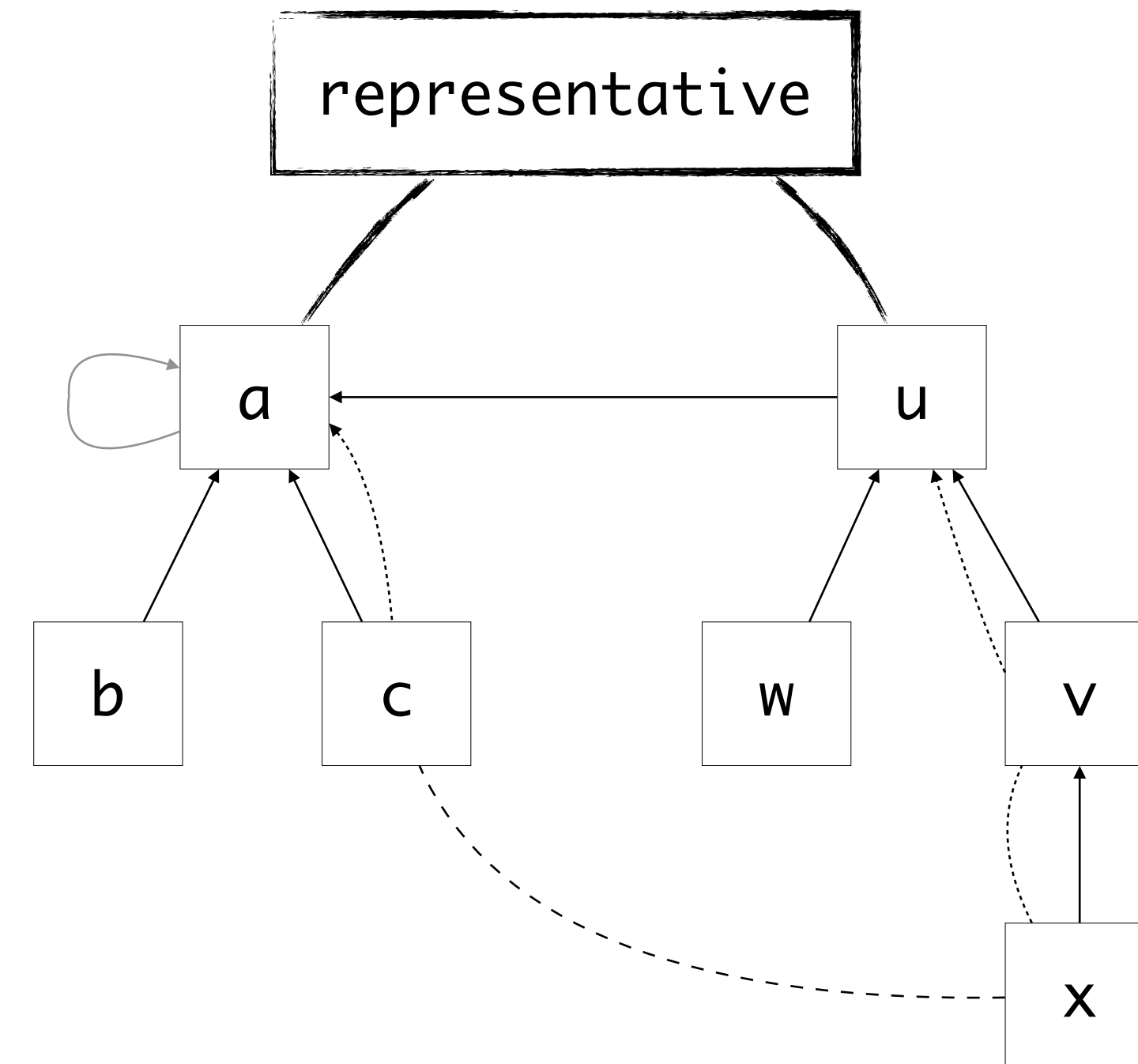
Set Representatives

```
FIND(a):  
  b := rep(a)  
  if b == a:  
    return a  
  else  
    return FIND(b)
```

```
UNION(a1,a2):  
  b1 := FIND(a1)  
  b2 := FIND(a2)  
  LINK(b1,b2)
```

```
LINK(a1,a2):  
  rep(a1) := a2
```

a == b
c == a
u == w
v == u
x == v
x == c



Path Compression

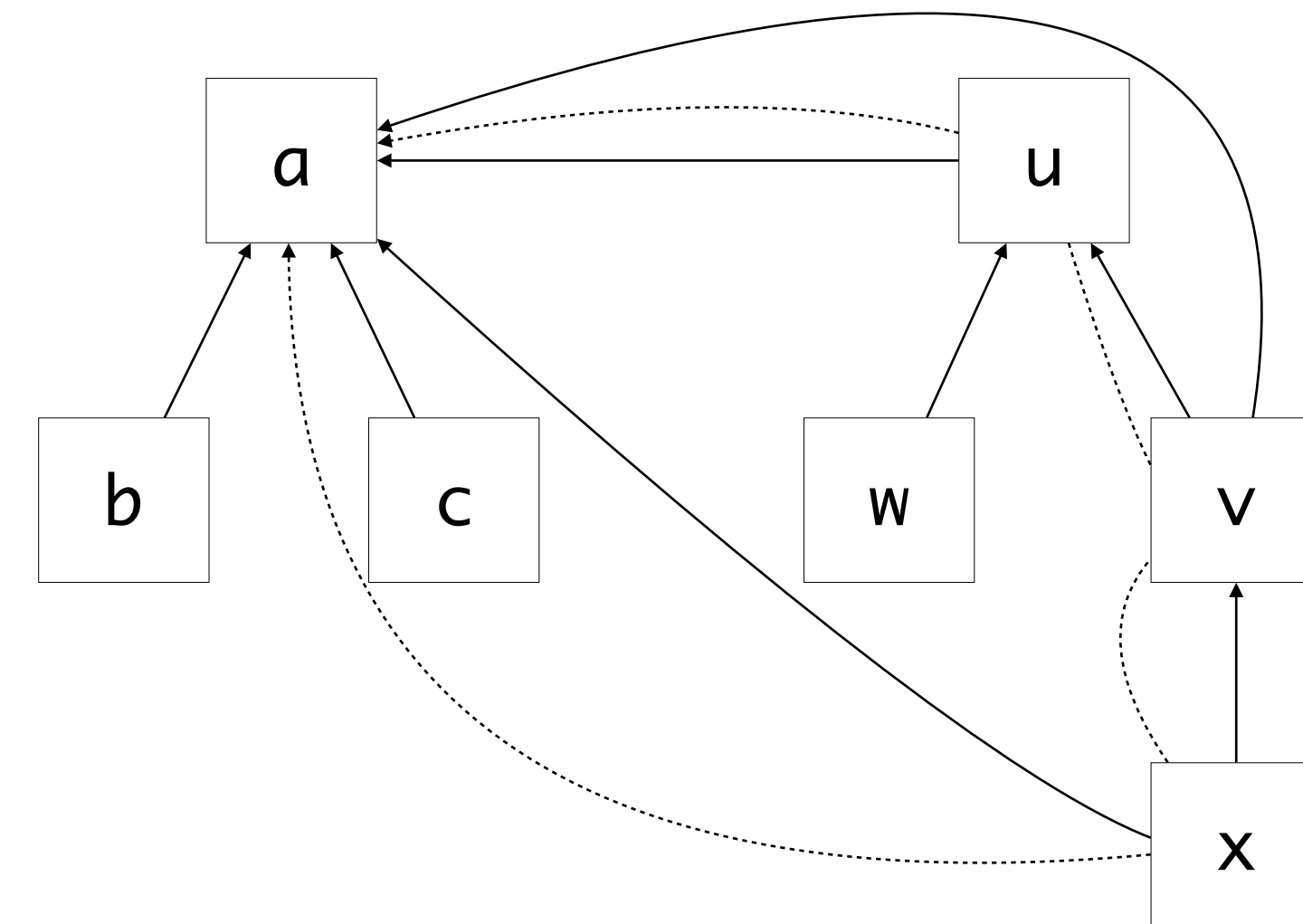
```
FIND(a):  
  b := rep(a)  
  if b == a:  
    return a  
  else  
    return FIND(b)
```

```
UNION(a1,a2):  
  b1 := FIND(a1)  
  b2 := FIND(a2)  
  LINK(b1,b2)
```

```
LINK(a1,a2):  
  rep(a1) := a2
```

...

```
x == b  
x == c  
x == w  
x == v
```



Tree Balancing

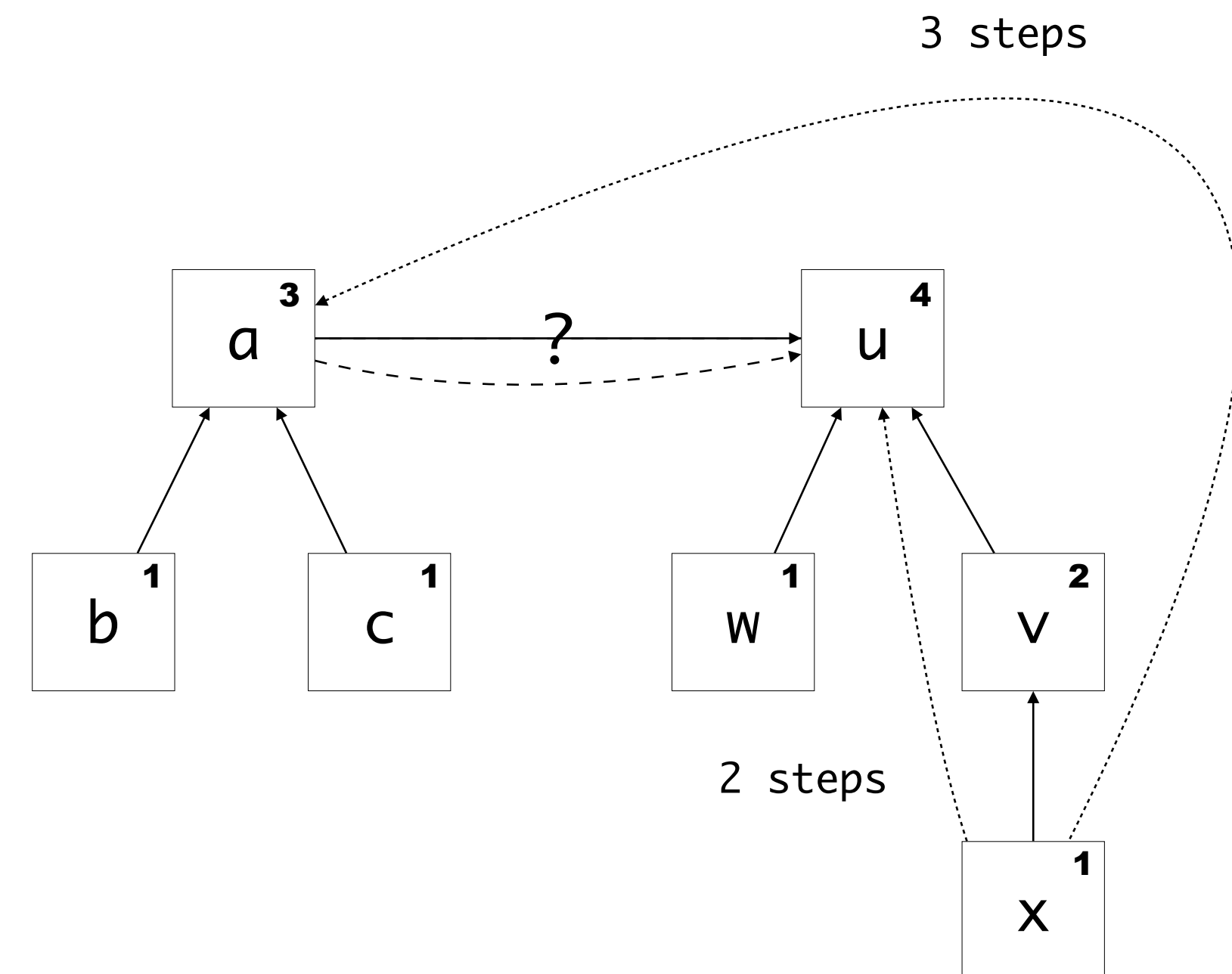
```
FIND(a):  
  b := rep(a)  
  if b == a:  
    return a  
  else  
    b := FIND(b)  
    rep(a) := b  
    return b
```

```
UNION(a1,a2):  
  b1 := FIND(a1)  
  b2 := FIND(a2)  
  LINK(b1,b2)
```

```
LINK(a1,a2):  
  rep(a1) := a2
```

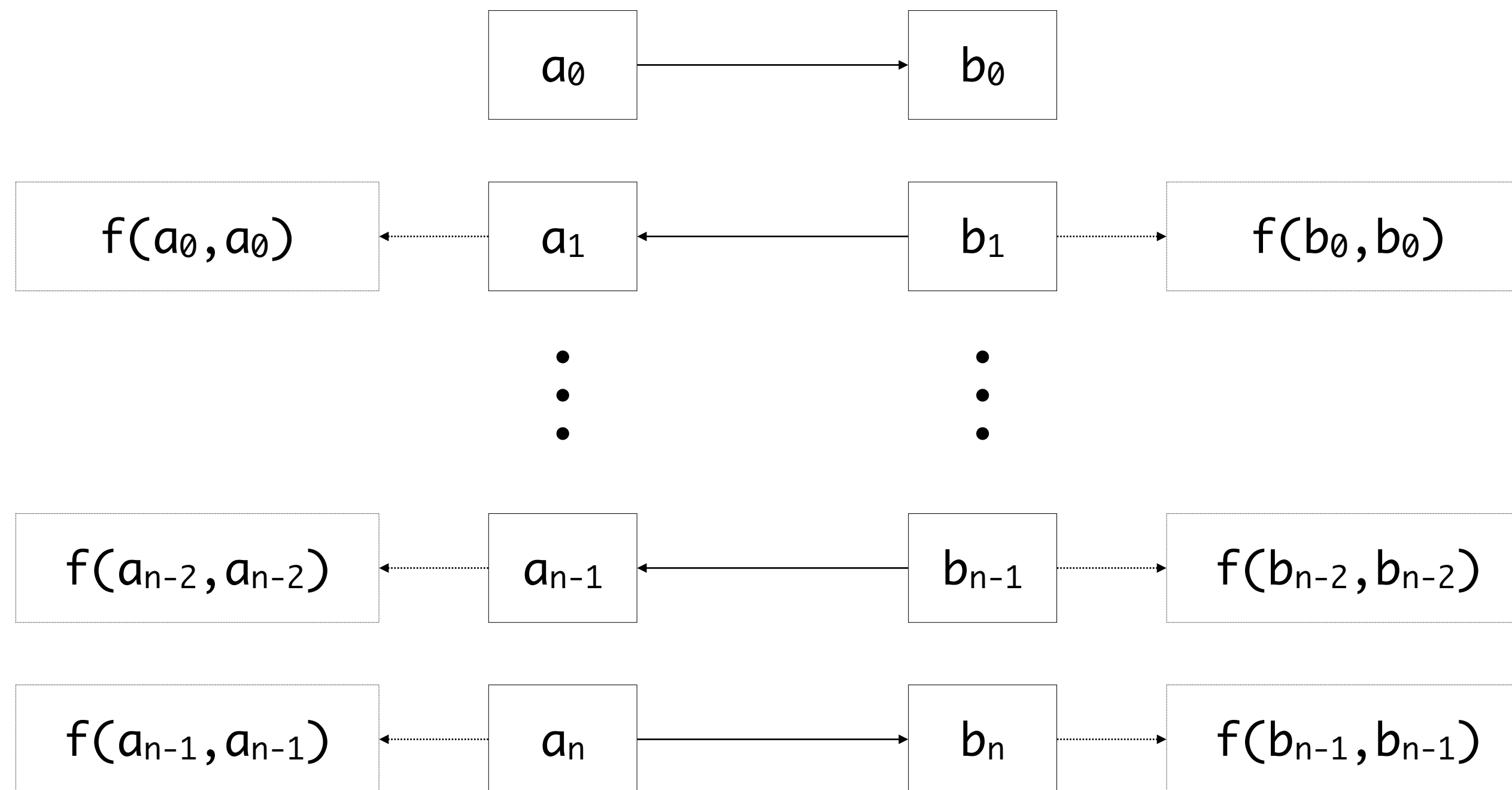
...

x == c



The Complex Case

$$h(a_1, \dots, a_n, f(b_0, b_0), \dots, f(b_{n-1}, b_{n-1}), a_n) == h(f(a_0, a_0), \dots, f(a_{n-1}, a_{n-1}), b_1, \dots, b_{n-1}, b_n)$$



$$\begin{aligned} a_n &== b_n \\ f(a_{n-1}, a_{n-1}) &== f(b_{n-1}, b_{n-1}) \\ a_{n-1} &== b_{n-1} \quad a_{n-1} == b_{n-1} \\ f(a_{n-2}, a_{n-2}) &== f(b_{n-2}, b_{n-2}) \\ &\vdots \\ a_1 &== b_1 \quad a_1 == b_1 \\ f(a_0, a_0) &== f(b_0, b_0) \\ a_0 &== b_0 \quad a_0 == b_0 \end{aligned}$$

How about occurrence checks? Postpone!

Union-Find

Main idea

- Represent unifier as graph
- One variable represent equivalence class
- Replace substitution by union & find operations
- Testing equality becomes testing node identity

Martelli, Montanari. An Efficient Unification Algorithm. TOPLAS, 1982

Optimizations

- Path compression make recurring lookups fast
- Tree balancing keeps paths short

Complexity

- Linear in space and almost linear (inverse Ackermann) in time
- Easy to extract triangular unifier from graph
- Postpone occurrence checks to prevent traversing (potentially) large terms

Conclusion

Summary

What is the meaning of constraints?

- Formally described by constraint semantics
- Semantics classifies solutions, but do not compute them
- Semantics is expressed in terms of other theories
 - Syntactic equality
 - Scope graph resolution

What techniques can we use to implement solvers?

- Constraint simplification
 - Simplification rules
 - Depends on built-in procedures to unify or resolve names
- Unification
 - Unifiers make terms with variables equal
 - Unification computes most general unifiers

What is the relation between solver and semantics?

- Soundness: any solution satisfies the semantics
- Completeness: if a solution exists, the solver finds it
- Principality: the solver computes most general solutions

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